

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau





(43) International Publication Date 25 April 2002 (25.04.2002)

PCT

(10) International Publication Number WO 02/33736 A1

- (51) International Patent Classification⁷:
 B44C 1/22
- H01L 21/00,
- (21) International Application Number: PCT/US01/31432
- (22) International Filing Date: 9 October 2001 (09.10.2001)
- (25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

09/692,730

19 October 2000 (19.10.2000) US

- (71) Applicant: FERRO CORPORATION [US/US]; 1000 Lakeside Avenue, Cleveland, OH 44114 (US).
- (72) Inventors: HER, Yie-Shein; 3430 West Lake Road, Canandaigua, NY 14424 (US). SRINIVASAN, Ramanathan; 333 West San Carlos Street, Suite 700, San Jose, CA 95110 (US). BABU, Suryadevara; 24 Haggerty Road, Potsdam, NY 13676 (US). RAMARAJAN, Suresh; 197 E. Mallard Drive, Apartment 227, Boise, ID 83706 (US).

- (74) Agent: CLARK, Kenneth, A.; Rankin, Hill, Porter & Clark LLP, 700 Huntington Building, 925 Euclid Avenue, Cleveland, OH 44115-1405 (US).
- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW.
- (84) Designated States (regional): European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR).

Published:

with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.



Al

(54) Title: CHEMICAL-MECHANICAL POLISHING SLURRY AND METHOD

(57) Abstract: The present invention provides a chemical-mechanical polishing slurry for use in removing a barrier layer during the fabrication of a damascene structure. Tslurry according to the invention includes an agent that suppresses the rate at which an underlying silicon-containing dielectric layer is removed. In the presently most preferred embodiment of the invention, the agent that suppresses the rate at which an underlying silicon-containing dielectric layer is removed is L-lysine and/or L-arginine. The present invention also provides a method of suppressing the removal rate of an underlying silicon-containing dielectric layer during the chemical-mechanical polishing of a barrier layer in a damascene structure. The method according to the invention includes polishing the barrier layer with a slurry comprising an agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed.

<u>Title</u>: Chemical-Mechanical Polishing Slurry and Method

Field of Invention

The present invention provides a chemical-mechanical polishing slurry for use in removing a barrier layer during the fabrication of a damascene structure, and a method of suppressing the removal rate of an underlying silicon-containing dielectric layer during the chemical-mechanical polishing of a barrier layer in a damascene structure using the slurry.

5

0

5

0

Background of the Invention

Integrated circuits are made up of millions of active devices formed in or on a substrate such as silicon or gallium arsenide. The active devices are typically isolated from each other using silicon-containing dielectric materials such as polysilicon, single-crystalline silicon, silicon dioxide, silicon-containing low-k inorganic and organic materials, phosphosilicate glass, borophosphosilicate glass, and the like. The active devices are usually formed in multiple layers that are interconnected to form functional circuits and components. Interconnection of active devices is typically accomplished through the use of well-known multilevel interconnection processes such as the process disclosed in Chow et al., U.S. Pat. 4,789,648.

Copper, aluminum and tungsten are preferred metals used to fabricate integrated circuits. These metals, however, have a tendency to diffuse into conventional silicon-containing dielectric materials. Once silicon-containing dielectric materials have been contaminated with metal atoms, the dielectric

2

constant is adversely affected and the integrated circuit may cease to function properly. Therefore, a barrier layer or liner film must be applied to the siliconcontaining dielectric layer in order to prevent metal diffusion.

5

0

5

:0

One of the presently preferred methods of fabricating integrated circuits having metal wiring and interconnects, which are also known as damascene structures, generally comprises providing interconnected metal wiring or metallization patterns in a discrete layer of silicon-containing dielectric film. The silicon-containing dielectric layer is etched or otherwise processed to pattern a series of trenches and holes therein. A thin barrier layer, generally not more than approximately 300 Å (30 nm) thick, is then deposited over the siliconcontaining dielectric layer. When the metal used to form the wiring pattern is copper, the preferred barrier layer comprises a thin film of tantalum and/or tantalum nitride. When the metal used to form the wiring pattern is aluminum or tungsten, the preferred barrier layer comprises a thin film of titanium and/or titanium nitride. Such barrier layers are commonly deposited by physical vapor deposition, which is also known as sputter deposition, or they may be deposited by chemical vapor deposition. The barrier layer coats the surfaces of the trenches and holes as well as the upper surface of the silicon-containing dielectric layer to prevent metal diffusion and also to provide good adhesion between the metallization layer and the silicon-containing dielectric layer. A layer of metal approximately 3,000-15,000 Å (300-1,500 nm) thick is then deposited over the barrier layer so as to completely fill the trenches and holes. The filled trenches thus form a network of metal lines whereas the filled holes form vias or

3

interconnects. The final step in the process of fabricating an integrated circuit, which is also known as a damascene process, is removing the metal layer and barrier layer down to the upper surface of the silicon-containing dielectric layer leaving only the metal filled trenches and holes. This is typically accomplished by chemical-mechanical polishing.

5

0

5

0

In a typical chemical-mechanical polishing process, the surface of the damascene structure is placed in direct contact with a rotating polishing pad at a controlled downward pressure. An abrasive and chemically reactive solution commonly referred to as a "slurry" is present between the pad and the surface of the damascene structure during polishing. The slurry initiates the polishing process by chemically reacting with the surface of the metal and/or barrier layer being polished. The polishing process is facilitated by the rotational movement of the pad relative to the substrate and the presence of the slurry at the film/pad interface. Polishing is continued in this manner until the desired metal and barrier layer are removed.

The composition of the slurry is an important factor in the chemical-mechanical polishing. If the chemical agents and abrasives in the slurry are selected properly, the slurry can be tailored to provide effective polishing of metals and barrier layers at desired polishing rates while minimizing surface imperfections, defects and corrosion and erosion. Furthermore, the polishing slurry can preferably provide controlled polishing selectivities to other materials used to construct the damascene structure.

4

One of the problems with known chemical-mechanical polishing slurries used to remove barrier layers in damascene structures is that they tend to remove the underlying silicon-containing dielectric layers at a relatively high rate. This makes it very difficult to remove all of the barrier layer without eroding or otherwise damaging the underlying silicon-containing dielectric layer. Because of this problem, in many instances the polishing of the barrier layer is stopped too soon, which results in a portion of the barrier layer remaining on the damascene structure. Conversely, if polishing of the barrier layer is stopped too late, the result is that significant portions of the underlying silicon-containing dielectric layer are removed. In either circumstance, the integrity and functionality of the integrated circuit can be adversely affected.

5

0

5

0

A need exists for a chemical-mechanical polishing slurry for use in removing barrier layers from damascene structures that reduces and/or eliminates this problem. Such a chemical-mechanical polishing slurry would preferably remove barrier layers at a high enough rate to insure acceptable throughput while at the same time avoid removing or damaging the underlying silicon-containing dielectric layers.

Summary of Invention

The present invention provides a chemical-mechanical polishing slurry for use in removing a barrier layer during the fabrication of a damascene structure. The slurry according to the invention comprises an agent that suppresses the rate at which an underlying silicon-containing dielectric layer is removed. In the presently most preferred embodiment of the invention, the agent that suppresses

5

the rate at which an underlying silicon-containing dielectric layer is removed is lysine and/or arginine.

The present invention also provides a method of suppressing the removal rate of an underlying silicon-containing dielectric layer during the chemical-mechanical polishing of a barrier layer in a damascene structure. The method according to the invention includes incorporating in a chemical-mechanical polishing slurry an agent that suppresses the rate at which the underlying silicon-containing dielectric layer is removed.

The foregoing and other features of the invention are hereinafter more fully described and particularly pointed out in the claims, the following description setting forth in detail certain illustrative embodiments of the invention, these being indicative, however, of but a few of the various ways in which the principles of the present invention may be employed.

Detailed Description of Preferred Embodiments

5

0

5

0

The present invention provides a chemical-mechanical polishing slurry for use in removing a barrier layer during the fabrication of a damascene structure. The slurry according to the invention comprises an agent that suppresses the rate at which an underlying silicon-containing dielectric layer is removed. An agent is considered to suppress the rate at which an underlying silicon-containing dielectric layer is removed within the meaning of such terms as used in the instant specification and in the appended claims if: (1) the agent, when present in a chemical-mechanical polishing slurry, reduces the rate at which an underlying silicon-containing dielectric layer is removed by 50% or more as

6

compared to when the agent is not present in the slurry; and (2) the agent, when present in a chemical-mechanical polishing slurry, does not reduce the rate at which the barrier layer is removed to an unacceptable level as compared to when the agent is not present in the slurry.

5

0

5

0

Preferably, the agent that suppresses the rate at which an underlying silicon-containing dielectric layer is removed comprises a compound having at least two functional amino groups (NH₂) and at least one functional carboxylic acid group (COOH). In the presently most preferred embodiment of the invention, the agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed is selected from the group consisting of lysine and arginine. As used throughout the specification and in the appended claims, reference to lysine and arginine is intended to refer to both the *D*- and *L*-forms of the compounds, as well as the salts and hydrated forms of such compounds. Furthermore, as used throughout the specification and in the appended claims, reference to lysine and arginine is intended to refer to precursors and derivatives of such compounds.

An effective amount of the amount of the agent that suppresses the rate at which an underlying silicon-containing dielectric layer is removed should be used to obtain the desired erosion suppression effect. Typically, the agent comprises from about 0.1% to about 5.0% by weight of the slurry. In a preferred embodiment of the invention, the agent comprises about 1.0% by weight of the slurry.

7

Optionally, the slurry according to the present invention can further comprise abrasive particles. However, it will be appreciated that for some applications it may be preferable for the slurry not to contain abrasive particles. In such circumstances, the mechanical polishing action is provided by the pressure of the polishing pad. When present, abrasive particles further assist in performing the function of mechanical grinding.

Abrasive particles that may be used in the slurry according to the present invention may comprise any one or a mixture of a variety of abrasive particles that are conventionally utilized in chemical-mechanical polishing slurries. Examples of suitable abrasive particles include alumina, ceria, copper oxide, diamond, iron oxide, nickel oxide, manganese oxide, silica, silicon carbide, silicon nitride, tin oxide, titania, titanium carbide, tungsten oxide, yttria, and zirconia, and combinations thereof. The presently most preferred abrasive for use in the invention are alumina and/or silica.

5

0

5

0

The abrasive particles preferably have a mean size ranging from about 0.02 to about 1.0 micrometer, with a maximum size of less than about 10 micrometers. It will be appreciated that while particle size is not per se critical, the use of very small abrasive particles can result in an unacceptably low polishing rate in some instances. It will also be appreciated that if, on the other hand, the particles are too large, then unacceptable scratching can occur on the surface of the article being polished. The abrasive particles can be present in the slurry in an amount up to about 60% by weight of the slurry, more preferably

8

from about 0.25% to about 30.0% by weight of the slurry, and optimally within the range of from about 0.5% to about 10.0% by weight of the slurry.

In some circumstances, it is advantageous for the slurry to further comprise an oxidizing agent. Suitable oxidizing agents for use in the invention include, for example, peroxides, persulfates, peroxydiphosphates, ferric nitrate, periodic acid, and periodates. Presently, the most preferred oxidizing agents for use in the slurry according to the invention are hydrogen peroxide, ammonium persulfate, and/or potassium sulfate. The oxidizing agent preferably comprises up to about 15.0% by weight of the slurry. More preferably, the oxidizing agent comprises from about 0.1% to about 10.0% by weight of the slurry.

5

0

5

0

The slurry according to the invention also preferably comprises a solvent. The preferred solvent for use in the invention is deionized water. Other solvents conventionally used in chemical-mechanical polishing slurries can also be used. In addition, the slurry may also contain optional surfactants, pH buffers, antifoaming agents, and dispersing agents, which are well known to those skilled in the art.

When the barrier layer to be removed comprises tantalum/tantalum nitride, it is preferable for the pH of the slurry to be adjusted to about 7.0 or higher, with a pH of about 10.0 being most preferred. The pH of the slurry can be raised using a base such as KOH. When the barrier layer to be removed comprises titanium/titanium nitride, it is preferable for the pH of the slurry to be adjusted to about 7.0 or less, with a pH of about 4.0 being most preferred. The

5

0

5

:0

9

pH of the slurry can be lowered using an acid such as HNO₃. Erosion of siliconcontaining dielectric layers is most severe at higher pH.

Preferably, the slurry according to the present invention is prepared by dispersing the agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed in a conventional solvent such as, for example, deionized water. If abrasive particles are to be included in the slurry, the slurry can be prepared in a number of ways. For example, the slurry may be prepared by dispersing the abrasive particles in a solvent either before or after the oxidizing agent and/or agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed has been added. The slurry may also be prepared as a two-component system (i.e., an abrasive dispersed in deionized water component and an oxidizing agent and agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed in deionized water component). The slurry may also be prepared in concentrated form needing only the addition of deionized water to dilute the concentrate (or concentrated components in a two-component system) to the desired level.

Alternatively, the slurry according to the present invention may be formed by incorporating a portion of the components of the slurry in a polishing pad. For example, the agent that suppresses the rate at which said underlying siliconcontaining dielectric oxide layer is removed could be incorporated directly in the polishing pad, either with or without abrasive particles, and deionized water and an oxidizing agent could then be added to the pad or the surface of the article

10

being polished to form the polishing slurry in situ. In another alternative embodiment, the abrasive particles could be bonded to the polishing pad, and the agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed and deionized water could be added either separately or together to the pad or the surface of the article being polished to form the polishing slurry in situ. It will be appreciated that the components of the slurry according to the invention could be combined in various ways to form the slurry in situ.

5

.0

5

0.

It is also possible to form the components of the slurry by combining chemical precursors together either before or at the time of polishing. Thus, as used in the specification and in the appended claims, the term "slurry" should be understood to refer to the components present at the interface between the polishing pad and the surface of the article being polished during chemical-mechanical polishing, and, unless otherwise stated, use of the term "slurry" is intended to encompass situations where precursors are combined to form the components of the slurry in situ.

The present invention is also directed to a method of suppressing the removal rate of an underlying silicon-containing dielectric layer during the chemical-mechanical polishing of a barrier layer in a damascene structure. The method comprises incorporating an agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed in a chemical-mechanical polishing slurry. The preferred agents that suppress the rate at which said

11

underlying silicon-containing dielectric layer is removed are lysine and/or arginine.

The following examples are intended only to illustrate the invention and should not be construed as imposing limitations upon the claims.

Example I

5

)

5

)

Slurries A, B, C, D, E, F, G, H, and I were formed by dispersing 4.0% by weight ceria having an average particle diameter of 350 nm in deionized water. Various amounts of several different amino acids were added to eight of the slurries as noted in **Table I** below. The pH of each slurry was adjusted to 10.0, unless otherwise noted in **Table I**, by adding KOH.

Blanket silicon wafers (6" diameter) having a 1.0 micron silicon dioxide film layer applied by tetraethylorthosilicate (TEOS) precursor chemical vapor deposition were separately polished with each of the nine slurries described above using a Strasbaugh 6CA polisher and a Rodel IC1400 K-grooved pad. The polishing conditions were: 6 psi down pressure; 0 psi back pressure; 40 rpm table speed; 40 rpm quill speed; 22°C temperature; and 340 cc/min slurry flow rate. The amount of silicon dioxide removed from the surface of the silicon wafer by chemical-mechanical polishing was measured using an optical interferometer to determine the rate of removal in terms of nm of silicon dioxide removed per minute. The results of the testing are shown in Table I below.

12 <u>Table I</u>

Slurry	рН	Agent (wt%)	TEOS Polishing Rate	
Α	10.0	NONE	489±9 nm/min	
В	10.0	1.0% glycine	547±30 nm/min	
С	10.0	4.0% glycine	420±30 nm/min	
D	10.0	8.0% glycine	410±10 nm/min	
E	9.5	4.0% glycine	417±32 nm/min •	
F	10.0	4.0% proline	536±10 nm/min	
G	10.0	1.0% proline	487±19 nm/min	
Н	10.8	1.0% arginine	23±3 nm/min	
1	10.0	1.0% lysine	71±3 nm/min	

Example I demonstrates that the presence of arginine and/or lysine in a chemical-mechanical polishing slurry has the effect of suppressing the rate of silicon-containing dielectric removal.

Example II

Slurries J and K were formed by dispersing 5.0% by weight alumina having an average particle diameter of 150 nm in deionized water, with 1.0% by weight ascorbic acid and 1.0% by weight hydrogen peroxide. Slurry J also included 1.0% by weight *L*-arginine. The pH of each slurry was adjusted to 10.0 by adding a sufficient amount of KOH.

Blanket silicon wafers (6" in diameter) that were previously coated with a 1.0 micron thick film layer of either vapor deposited tetraethylorthosilicate (TEOS), copper, tantalum, or tantalum nitride, were then polished with slurries J and K described above using a Strasbaugh 6EC polisher and an IC1000/Suba

5

3

5

0

13

IV k-grooved stacked pad. The polishing conditions were: 4 psi down pressure; 1 psi back pressure; 40 rpm table speed; 40 rpm quill speed; 20°C temperature; and 300 cc/min slurry flow rate. The amount of each film layer removed from the surface of the silicon wafers by chemical-mechanical polishing was measured using an optical interferometer to determine the rate of removal in terms of Å removed per minute. The results of the testing are shown in **Table II** below.

5

0

5

<u></u>20

Table II

Slurry	Cu Removal Rate	Ta Removal Rate	TaN Removal Rate	TEOS Removal Rate
J	3180 Å/min	165 Å/min	179 Å/min	0 Å/min
K	4199 Å/min	535 Å/min	463 Å/min	35 Å/min

Example II demonstrates that the presence of arginine in a chemical-mechanical polishing slurry substantially suppresses the rate at which a silicon-containing dielectric layer is removed during chemical-mechanical polishing, and that the presence of arginine in the slurry does not suppress the rate at which barrier films are removed to an unacceptably low level.

Additional advantages and modifications will readily occur to those skilled in the art. Therefore, the invention in its broader aspects is not limited to the specific details and illustrative examples shown and described herein. Accordingly, various modifications may be made without departing from the spirit or scope of the general inventive concept as defined by the appended claims and their equivalents.

What is Claimed:

5

0

5

0

1. A chemical-mechanical polishing slurry for use in removing a barrier layer during the fabrication of a damascene structure comprising an agent that suppresses the rate at which an underlying silicon-containing dielectric layer is removed.

2. The chemical-mechanical polishing slurry according to claim 1 wherein said agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed comprises a compound having at least two functional amino groups (NH₂) and at least one functional carboxylic acid group (COOH).

- 3. The chemical-mechanical polishing slurry according to claim 2 wherein said agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed is selected from the group consisting of lysine and arginine.
- 4. The chemical-mechanical polishing slurry according to claim 2 wherein said agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed comprises by weight from about 0.1% to about 5.0% of said slurry.
- 5. The chemical-mechanical polishing slurry according to claim 2 wherein said barrier layer comprises tantalum and/or tantalum nitride.
- 6. The chemical-mechanical polishing slurry according to claim 5 wherein the pH of said slurry is about 7.0 or higher.

7. The chemical-mechanical polishing slurry according to claim 2 wherein said barrier layer comprises titanium and/or titanium nitride.

- 8. The chemical-mechanical polishing slurry according to claim 7 wherein the pH of said slurry is about 7.0 or lower.
- 9. The chemical-mechanical polishing slurry according to claim 2 further comprising abrasive particles.

5

0

5

0

- 10. The chemical-mechanical polishing slurry according to claim 9 wherein said abrasive particles are selected from the group consisting of alumina, ceria, copper oxide, diamond, iron oxide, nickel oxide, manganese oxide, silica, silicon carbide, silicon nitride, tin oxide, titania, titanium carbide, tungsten oxide, yttria, and zirconia.
- 11. The chemical-mechanical polishing slurry according to claim 2 further comprising an oxidizing agent.
- 12. The chemical-mechanical polishing slurry according to claim 11 wherein said oxidizing agent is selected from the group consisting of peroxides, persulfates, peroxydiphosphates, ferric nitrate, periodic acid, and periodates.
- 13. The chemical-mechanical polishing slurry according to claim 2 further comprising a solvent.
- 14. The chemical-mechanical polishing slurry according to claim 13 wherein said solvent comprises deionized water.
- 15. The slurry according to claim 2 wherein said underlying siliconcontaining dielectric layer is selected from the group consisting of polysilicon,

single-crystalline silicon, silicon dioxide, silicon-containing low-k inorganic and organic materials, phosphosilicate glass, and borophosphosilicate glass.

16. A chemical-mechanical polishing slurry for use in removing a tantalum and/or tantalum nitride or titanitum and/or titanium nitride barrier layer during the fabrication of a damascene structure, said slurry comprising:

an agent that suppresses the rate at which an underlying siliconcontaining dielectric layer is removed, said agent comprising a compound having at least two functional amino groups (NH₂) and at least one functional carboxylic acid group (COOH).

0

5

- 17. The chemical-mechanical polishing slurry according to claim 16 wherein said agent that suppresses the rate at which an underlying silicon-containing dielectric layer is removed is selected from the group consisting of lysine and arginine.
- 18. The chemical-mechanical polishing slurry according to claim 16 wherein said agent that suppresses the rate at which an underlying silicon-containing dielectric layer is removed comprises from about 0.1% to about 5.0% by weight of said slurry.
- 19. The chemical-mechanical polishing slurry according to claim16 further comprising:

0

5

abrasive particles;

an oxidizing agent; and

a solvent.

20. The chemical-mechanical polishing slurry according to claim 19 wherein said oxidizing agent is selected from the group consisting of peroxides, persulfates, peroxydiphosphates, ferric nitrate, periodic acid, and periodates.

21. A method of suppressing the removal rate of an underlying siliconcontaining dielectric layer during the chemical-mechanical polishing of a barrier layer in a damascene structure, said method comprising polishing said barrier layer using a chemical-mechanical polishing slurry comprising an agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed.

5

0

5

- 22. The method according to claim 21 wherein said agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed comprises a compound having at least two functional amino groups (NH₂) and at least one functional carboxylic acid group (COOH).
- 23. The method according to claim 22 wherein said agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed is selected from the group consisting of lysine and arginine.
- 24. The method according to claim 22 wherein said agent that suppresses the rate at which said underlying silicon-containing dielectric layer is removed comprises from about 0.1% to about 5.0% by weight of said slurry.

INTERNATIONAL SEARCH REPORT

In.....onal application No. PCT/US01/51452

A. CLASSIFICATION OF SUBJECT MATTER						
IPC(7) : : Holl 21/00; B44C 1/22 US CL :US CL : 438/690, 691, 692, 693; 252/79.1, 79.2						
According to International Patent Classification (IPC) or to both national classification and IPC						
B. FIELDS SEARCHED						
Minimum documentation searched (classification system followed by classification symbols)						
U.S. : US CL : 438/690, 691, 692, 693; 252/79.1, 79.2						
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched .						
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)						
EAST						
C. DOCUMENTS CONSIDERED TO BE RELEVANT						
Category Citation of document, with indication, where appropriate, of the relevant passages Relevant to claim No.						
X US 6,194,317 B1 (KAISAKI et al) 27 February 2001, col.7, lines 37-39; col. 14, lines 40-61; col. 14, lines 40-61; col. 8, lines 60-65; col. 15, lines 22-25; col. 4, lines 36-37.						
Y US 6,136,711 A (GRUMBINE et al) 24 October 2000, col. 6, lines 4, 18, 24 9-25.						
Further documents are listed in the continuation of Box C. See patent family annex.						
Special categories of cited documents: Special categories of cited documents:						
"A" document defining the general state of the art which is not considered the principle or theory underlying the invention to be of particular relevance						
"E" earlier document published on or after the international filing date "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step						
"L" document which may throw doubts on priority claim(s) or which is when the document is taken alone eited to establish the publication date of another eitation or other						
special reason (as specified) "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined "O" document referring to an oral disclosure, use, exhibition or other means special reason (site claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being						
"P" document published prior to the informational filing date but later "A" document member of the same patent family than the priority date claimed						
Date of the actual completion of the international search Date of mailing of the international search report						
26 DECEMBER 2001 03 JAN 2002						
Name and mailing address of the ISA/US Commissioner of Patents and Trademarks Box PCT Washington, D.C. 20231 Authorized officer LAN VINH						
Box PCT Washington, D.C. 20231 LAN VINH						